

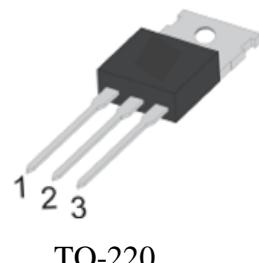


BT153-800HAY

Silicon Controlled Rectifier

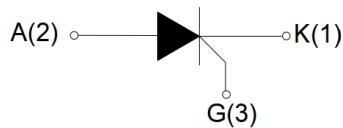
Features

- Blocking Voltage to 800V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant & HF
- High Dv/Dt Rate
- $I_{T(RMS)}$ to 25A of SCR



TO-220

Pin Configuration



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	°C
Operating junction temperature range	T_j	-40~125	°C
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_C=100^\circ\text{C}$)	$I_{T(RMS)}$	25	A
Average on-state current (180° conduction angle)	$I_{T(AV)}$	16	A
Non repetitive surge peak on-state current (full cycle, $F=50\text{Hz}$)	I_{TSM}	250	A
I^2t value for fusing ($t_p=10\text{ms}$)	I^2t	312	A^2s
Critical rate of rise of on-state current ($IG=2\times IGT$)	dI/dt	50	$\text{A}/\mu\text{s}$
Peak gate current	I_{GM}	4	A
Average gate power dissipation	$P_{G(AV)}$	1	W

Thermal Resistance(between Junction and Case) @TO-220	$R_{\theta(J-C)}$	1.2 (Typ.)	°C/W
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Electronics Characteristics (T_c=25°C Unless otherwise specified)

Parameter	Symbol	Min	Typ.	Max.	Unit
Gate Trigger Current (Continuous dc)@V _D =12V, R _L =33Ω	I _{GT}	-	-	30	mA
Gate Trigger Voltage (Continuous dc) @V _D =12V, R _L =33Ω	V _{GT}	-	-	1.3	V
Gate non-trigger voltage@V _D =V _{DRM} ,T _j =125°C	V _{GD}	0.2	-	-	V
Holding Current@I _T =500mA	I _H	-	-	50	mA
Latching Current@I _G =1.2I _{GT}	I _L	-	-	80	mA
Critical Rate-of-Rise of Off State Voltage@V _D =0.66×V _{DRM} , T _j =125°C, Gate Open	dV/dt	500	-	-	V/μs
Peak Forward On-State Voltage@I _{TM} =30A, t _p =380μs, T _j =25°C	V _{TM}	-	-	1.6	V
Peak Repetitive Forward@V _{DRM} =V _{RRM} ,T _j =25°C	I _{DRM}	-	-	10	μA
Reverse Blocking Current@V _{DRM} =V _{RRM} ,T _j =125°C	I _{RRM}	-	-	1	mA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus Average on-state current

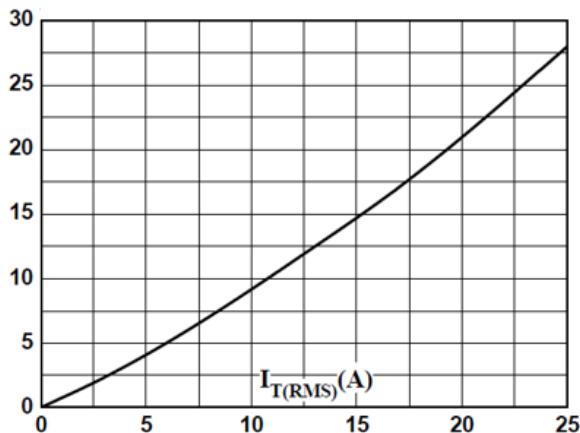
 $P(w)$ 

FIG.2: RMS on-state current versus case temperature

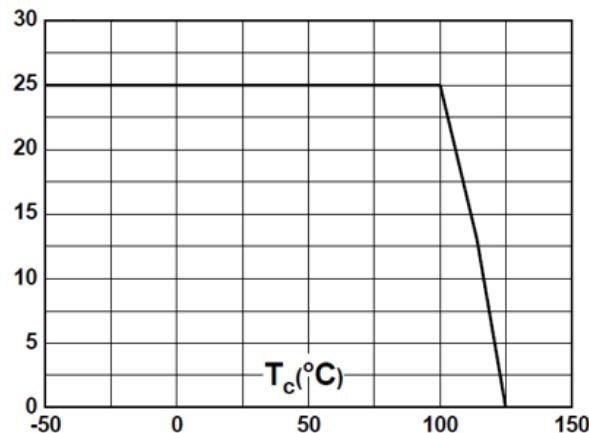
 $I_{T(RMS)}(A)$ 

FIG.3: Surge peak on-state current versus number of cycles

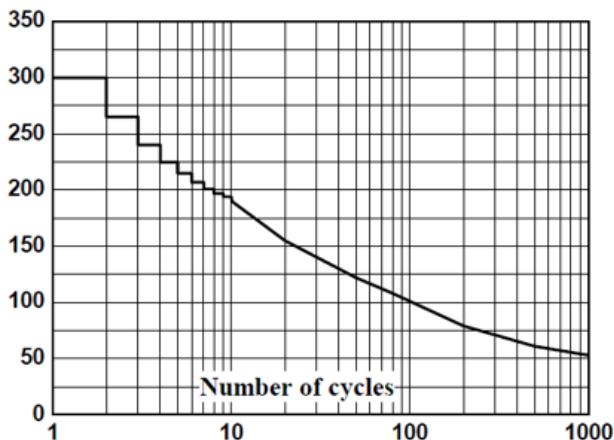
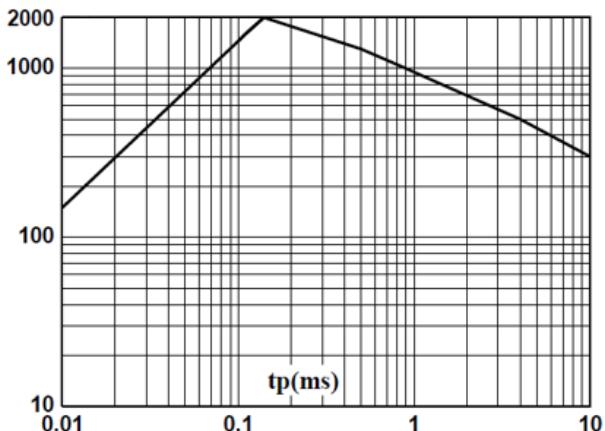
 $I_{TSM}(A)$ FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $tp < 10ms$ $I_{TSM}(A)$ 

FIG.4: On-state characteristics (maximum values)

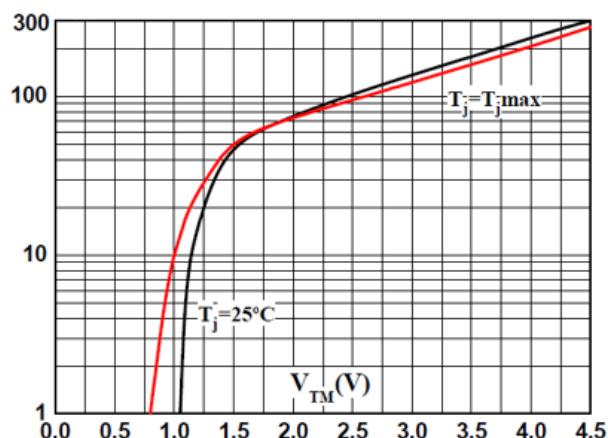
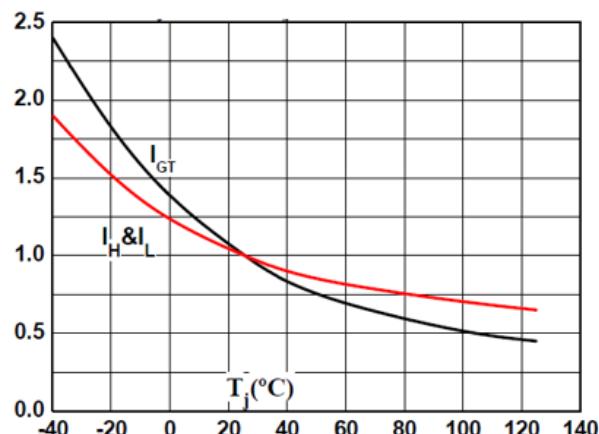
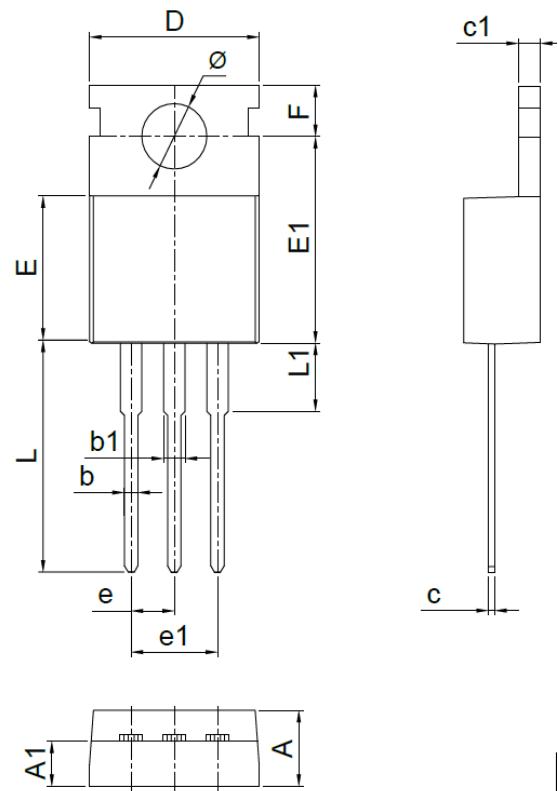
 $I_{TM}(A)$ 

FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

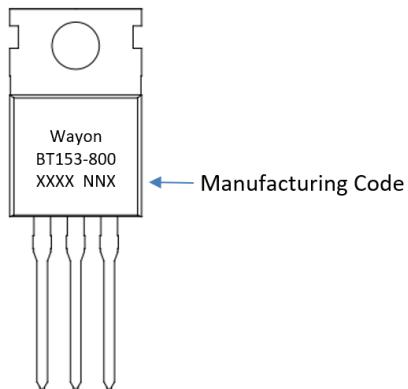
 $I_{GT}, I_H, I_L(T_j)/I_{GT}, I_H, I_L(T_j=25^\circ\text{C})$ 

Outline Drawing- TO-220

SYMBOL	MM		
	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	2.52	2.67	2.82
b	0.71	0.81	0.91
b1	1.17	1.27	1.37
c	0.30	0.40	0.50
c1	1.17	1.27	1.37
D	9.90	10.00	10.20
E	8.50	8.70	8.90
E1	12.00	12.25	12.50
e	2.44	2.54	2.64
e1	4.88	5.08	5.26
F	2.60	2.75	2.80
L	13.00	13.50	14.00
L1	3.80	4.00	4.20
Φ	3.75	3.85	3.95



Marking Code:



Package Information

Package	Base qty.	Delivery mode
TO-220	50	Tube

Contact Information

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