



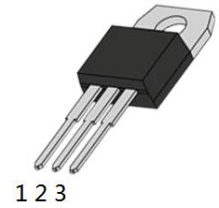
BT151 Series

Features

- Blocking Voltage to 650V/800 V
- Glass Passivated Surface for Reliability and Uniformity
- RoHS Compliant
- High dV/dt Rate
- $I_{T(RMS)}$ to 12A of Triacs

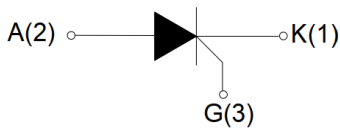


TO-252-4R



TO-220B(No-Ins)

Pin Configuration



Absolute Maximum Ratings (Tc=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40~150	°C
Operating junction temperature range	T_j	-40~125	°C
Repetitive peak off-state voltage (Tj=25°C)	V_{DRM}	650/800	V
Repetitive peak reverse voltage (Tj=25°C)	V_{RRM}	650/800	V
RMS on-state current	$I_{T(RMS)}$	12	A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	120	A
I^2t value for fusing (tp=10ms)	I^2t	72	A ² s
Critical rate of rise of on-state current (IG=2×IGT)	dI/dt	50	A/μs
Peak gate current	I_{GM}	2	A
Average gate power dissipation	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	5	W

Thermal Resistance(between Junction and Case) @TO-220B(Non-Ins)	$R_{\theta(J-C)}$	1.3 (Typ.)	°C/W
Thermal Resistance(between Junction and Case) @TO-252-4R	$R_{\theta(J-C)}$	1.4 (Typ.)	°C/W

Electronics Characteristics (T_c=25°C Unless otherwise specified)

Parameter	Symbol	Min	Typ.	Max.	Unit
Gate Trigger Current (Continuous dc) @V _D =12V, R _L =33Ω	I _{GT}	-	4	15	mA
Gate Trigger Voltage (Continuous dc) @V _D =12V, R _L =33Ω	V _{GT}	-	0.75	1.5	V
Gate non-trigger voltage @V _D =V _{DRM} , T _j =110°C	V _{GD}	0.2	-	-	V
Holding Current @I _T =50mA	I _H	-	-	30	mA
Latching Current @I _G =1.2I _{GT}	I _L	-	-	40	mA
Critical Rate-of-Rise of Off State Voltage @V _D =0.66×V _{DRM} , T _j =125°C, Gate Open	dV/dt	200	-	-	V/μs
Peak Forward On-State Voltage @I _{TM} =23A, t _p =380μs, T _j =25°C	V _{TM}	-	-	1.75	V
Peak Repetitive Forward @V _{DRM} =V _{RRM} , T _j =25°C	I _{DRM}	-	-	5	μA
Reverse Blocking Current @V _{DRM} =V _{RRM} , T _j =110°C	I _{RRM}	-	-	500	μA

Note: The above typical parameters or typical characteristics are only indicative and do not make specific guarantees. If detailed values are required, additional communication and provision are required.

FIG.1: Maximum power dissipation versus RMS on-state current

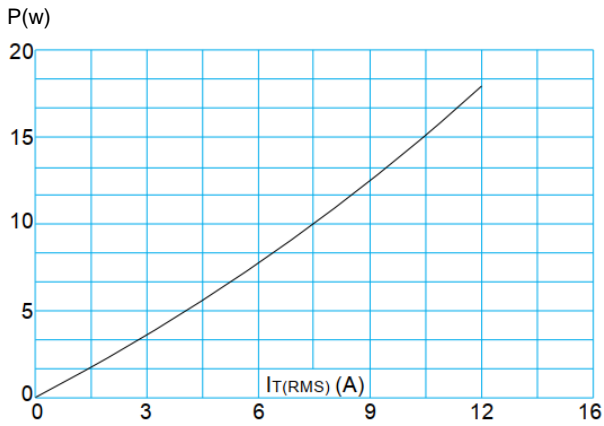


FIG.3: Surge peak on-state current versus number of cycles

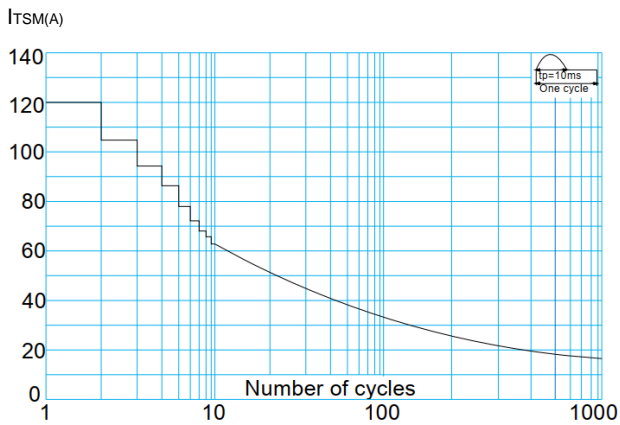


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of $I^2 t$ ($di/dt < 50\text{A}/\mu\text{s}$)

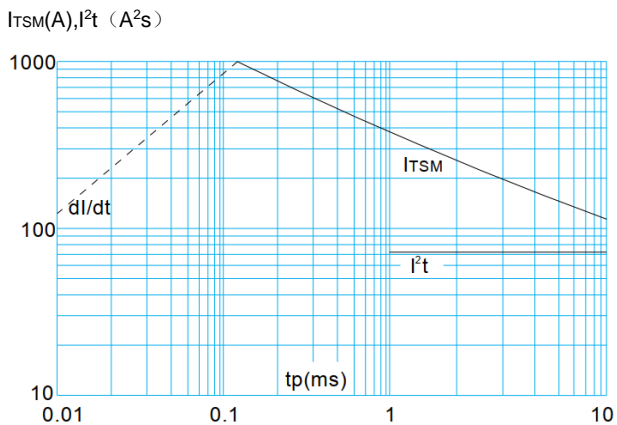


FIG.2: RMS on-state current versus case temperature in different packaging

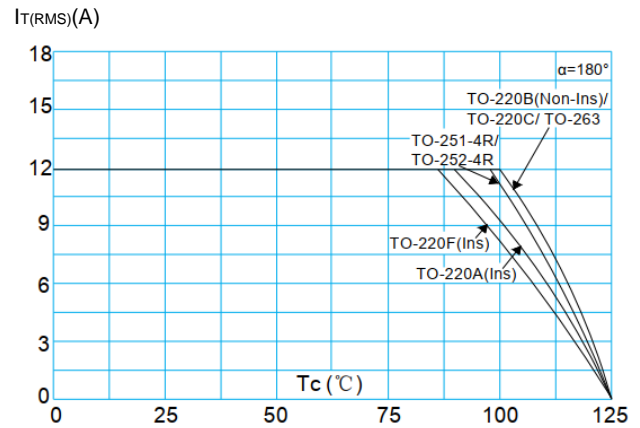


FIG.4: On-state characteristics (maximum values)

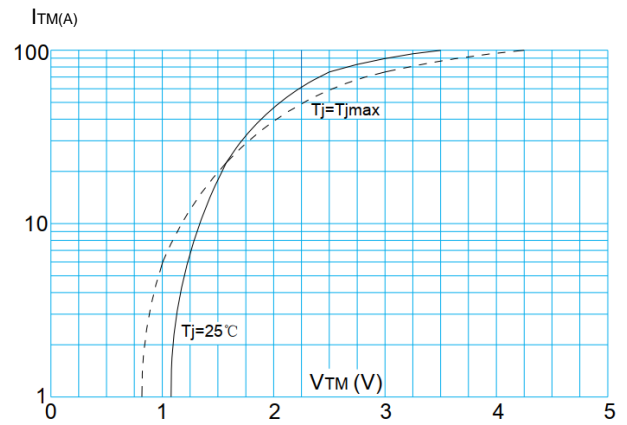
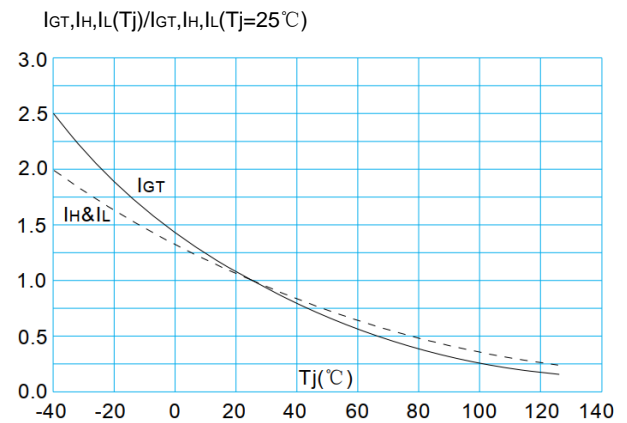
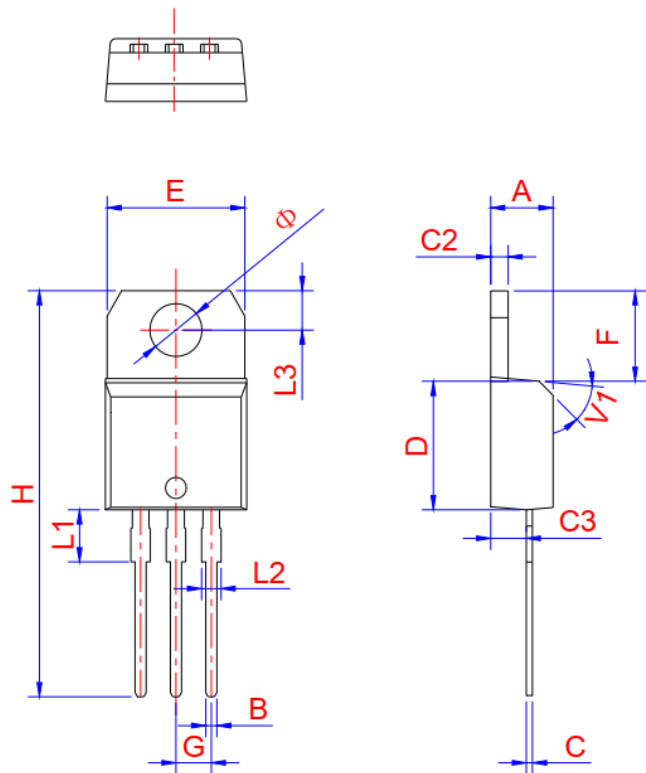


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



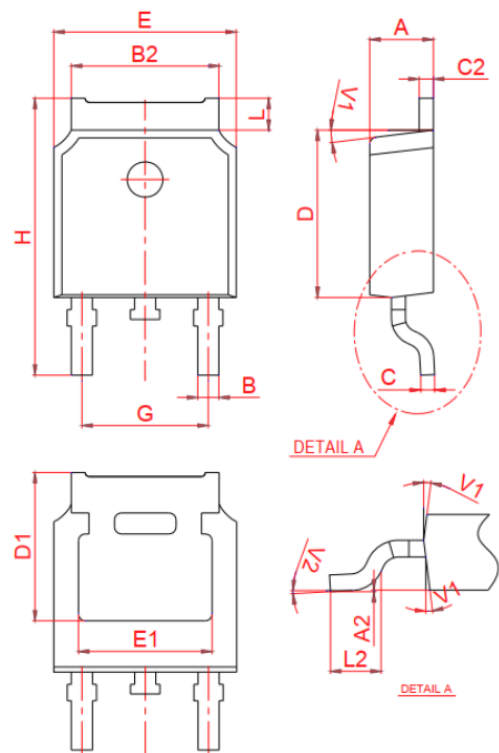
Outline Drawing- TO-220B Non-Ins

SYMBOL	MM		
	MIN	NOM	MAX
A	4.4	4.47	4.6
B	0.61		0.88
C	0.46	0.50	0.7
C2	1.21	1.27	1.32
C3	2.4		2.72
D	8.6		9.7
E	9.8		10.4
F	6.55		6.95
G		2.54	
H	28		29.8
L1		3.75	
L2	1.14		1.7
L3	2.65		2.95
V1		45°	
Φ	3.7	3.75	3.8

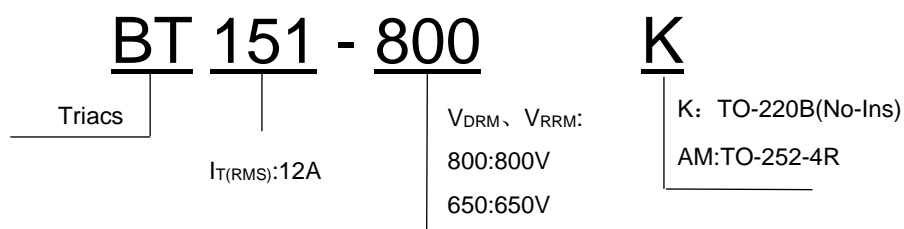


Outline Drawing- TO-252-4R

SYMBOL	MM		
	MIN	NOM	MAX
A	2.2		2.4
A2	0		0.2
B	0.66		0.9
B2	5.1		5.46
C	0.46		0.58
C2	0.43		0.61
D	5.9		6.3
D1	5.30REF		
E	6.4		6.8
E1	4.63		
G	4.372		4.772
H	9.4		10.5
L	0.88		1.28
L2	1.35		1.75
V1		7°	
V2	0°		8°



Part Number System



Package Information

Package	Base qty.	Delivery mode
TO-220B(No-Ins)	50	Tube
TO-252-4R	2500	Reel

Contact Information

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