

Feature

- The chips are electrically insulated from bottom plate, 2500V AC voltage
- Complete pressure connection structure, with excellent temperature Characteristics and power cycling capacity
- Forced air cooling for modules below 400A and air cooling or water Cooling for modules above 500A

Typical application

- DC power supplies of appliances and devices
- AC and DC motor control, Soft starting for motors
- Various rectifying power supply
- Electric welders, Frequency transformers, Battery charging and discharging

$I_{F(AV)}$	160A
V_{RRM}	500-2500V
I_{FSM}	6 KA
I^2t	184 $10^3 a^2 s$

SYMBOL	CHARCTERISTIC	TEST CONDITIONS	T _J (℃)	VALUE		UNIT
				Min	Max	
I _{F(AV)}	Mean forward current	180° half sine wave, 50HZ Single heat sink, T _C =98℃	150		160	A
I _{T(RMS)}	RMS current		150		251	A
V _{RRM}	Repetitive peak reverse voltage	V _{DRM} &V _{RRM} tp=10ms V _{DSM} &V _{RSM} =V _{DRM} &V _{RRM} +200V	150	500	2500	V
I _{RRM}	Repetitive peak current	V _{RM} =0V _{RRM}	150		12	mA
I _{FSM}	Surge on-state current	10ms half sine wave V _R =0.6V _{RRM}	150		6.00	KA
I ² t	I ² t for fusing coordination				184	A ² S*10
V _{TO}	Threshold voltage		150		0.80	V
r _T	On-state slop resistance				1.35	mΩ
V _{FM}	Peak on-state voltage	I _{TM} =480A	25		1.20	V
R _{th(j-c)}	Thermal impedance node to the shell	180 ° sine wave, single heat sink			0.230	℃/W
R _{th(c-h)}	Thermal impedance (shell to powder)	180 ° sine wave, single heat sink			0.08	℃/W
V _{iso}	Insulation voltage			2500		V
F _M	Mounting force (M6)				6	N-m
	Mounting force (M6)				6	N-m
T _{stq}	Stored temperature			-40	125	℃
W _t	Weight		190			g
Outline						

Peak forward Voltage Vs. Peak forward Current

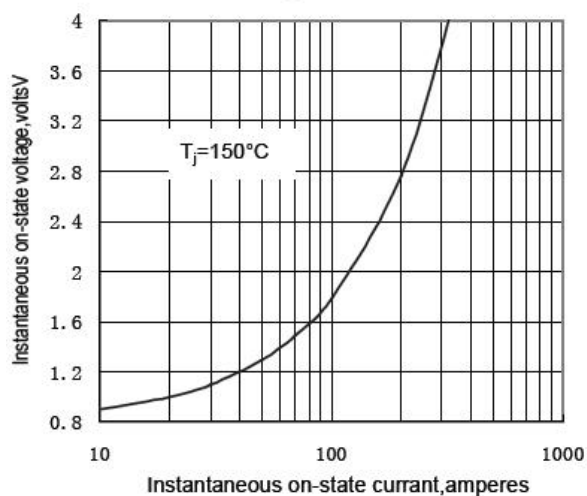


Fig.1

Max. junction To case Thermal Impedance Vs. Time

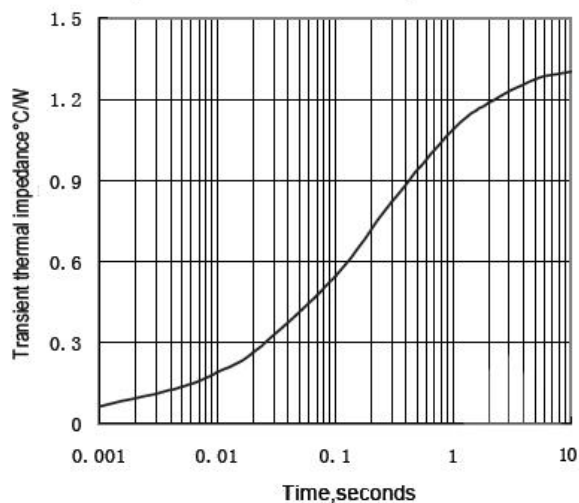


Fig.2

Max. Power Dissipation Vs. Mean forward Current

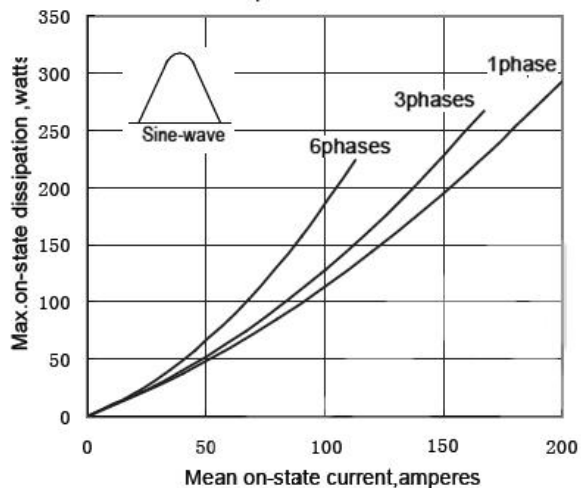


Fig.3

Max. heatsink Temperature Vs. Mean On-state Current

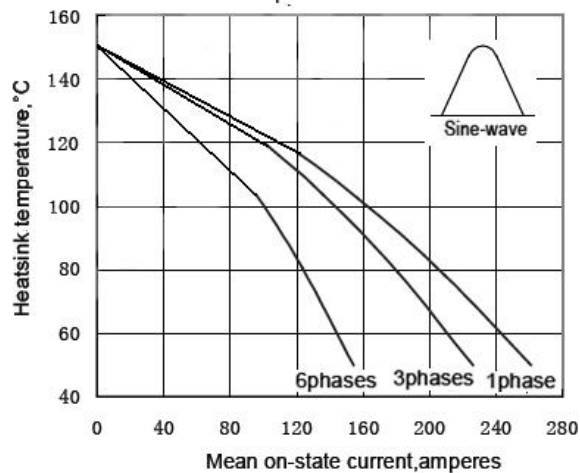


Fig.4

Max. Power Dissipation Vs. Mean On-state Current

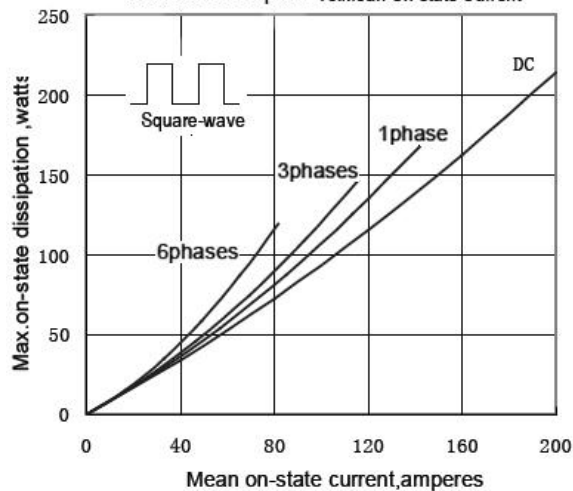


Fig.5

Max. case Temperature Vs. Mean forward Current

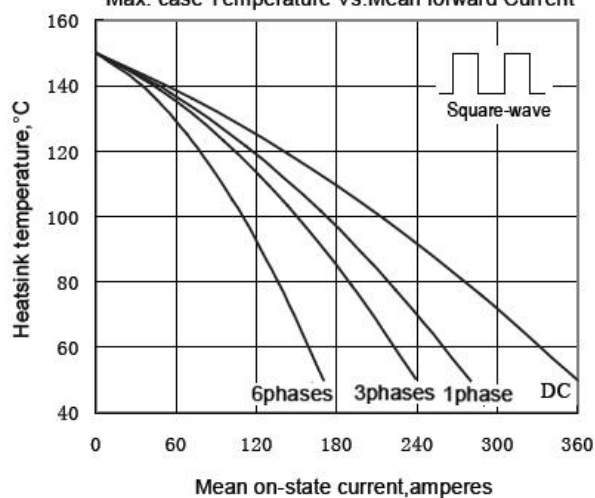


Fig.6

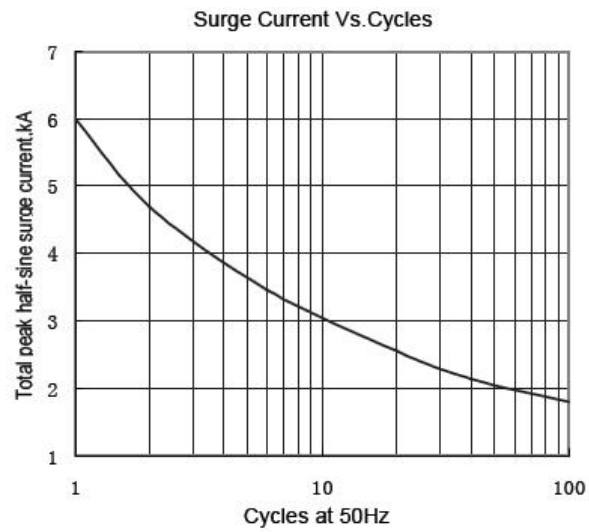


Fig.7

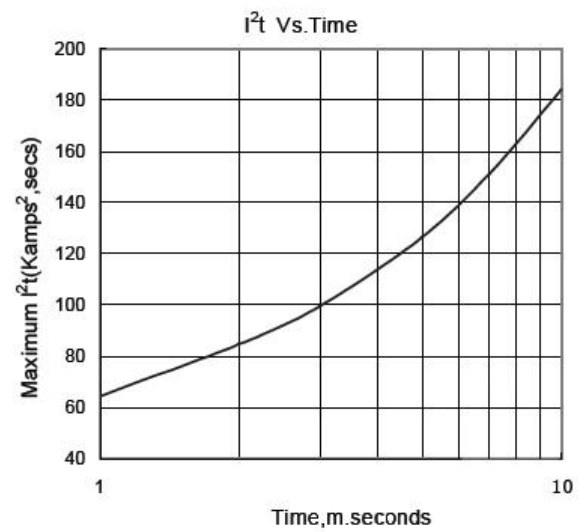
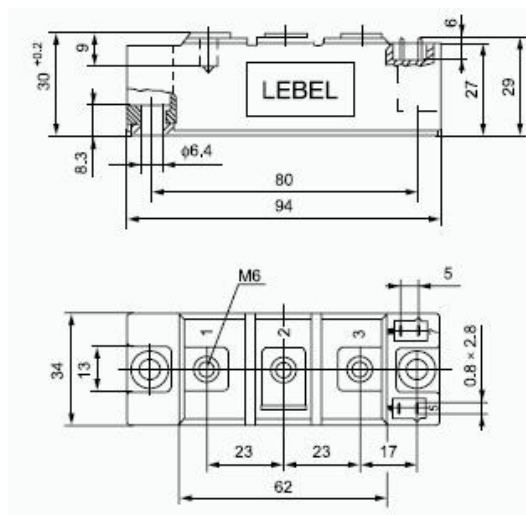


Fig.8

Outline in MM :



Circuit Drawing:

