

STM32 EMBEDDED 7.0" DISPLAY DATASHEET

Rev.0.1 2022-03-28

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	7.0	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	179.96 x 119.00 x 19.67	mm
Active Area (W x H)	154.21 x 85.92	mm
Pixel Pitch (W x H)	0.1506 x 0.1432	mm
Resolution	1024 x 600	/
Brightness	800	cd/m²
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Driver IC of Board	STM32H747XIH6	/
External SDRAM	64Mbit (32-bit access)	/
External Flash Memory	512Mbit	/
Supply Voltage for Module	6.0 - 36.0	V
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Weight	TBD	g

Note 1: RoHS3 compliant

Note 2: LCM weight tolerance: ± 5%.



1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.1	2022-03-28	Preliminary version	



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3. MODULE CLASSIFICATION INFORMATION

		70							
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	70 – 7.0"
4.	MODEL SERIAL NO.	H – High Brightness, IPS TFT
5.	RESOLUTION	S – 1024 x 600 px
6.	LCD+CONTROL BOARD	S – STM32 Embedded Display MCU STM32H747XIH6
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	00 – (00-99)



4. uxTouch ASSEMBLY GUIDE

uxTouch are LCD TFT displays with specially designed projected capacitive touch panels. uxTouch display can be mounted without any holed in the housing. Our standard uxTouch displays include double-sided adhesive tape (DST) to stick TFT easily to the housing. Basic series include 4.3", 5.0",7.0" and 10.1" display sizes.

uxTouch models with double-side adhesive tape can be mounted by connecting the glass to the housing. Riverdi recommends using support brackets assembled to display's back. An additional support will stiffen the whole structure and minimize the influence of external factors such as vibration. Figure 1 below show examples of using support elements.

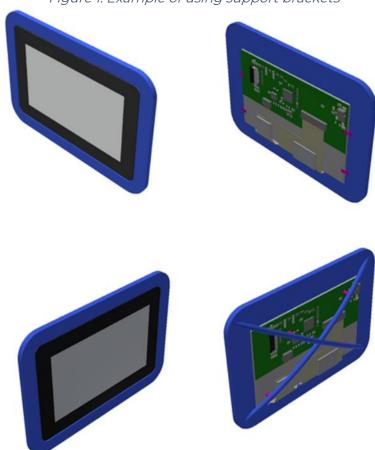
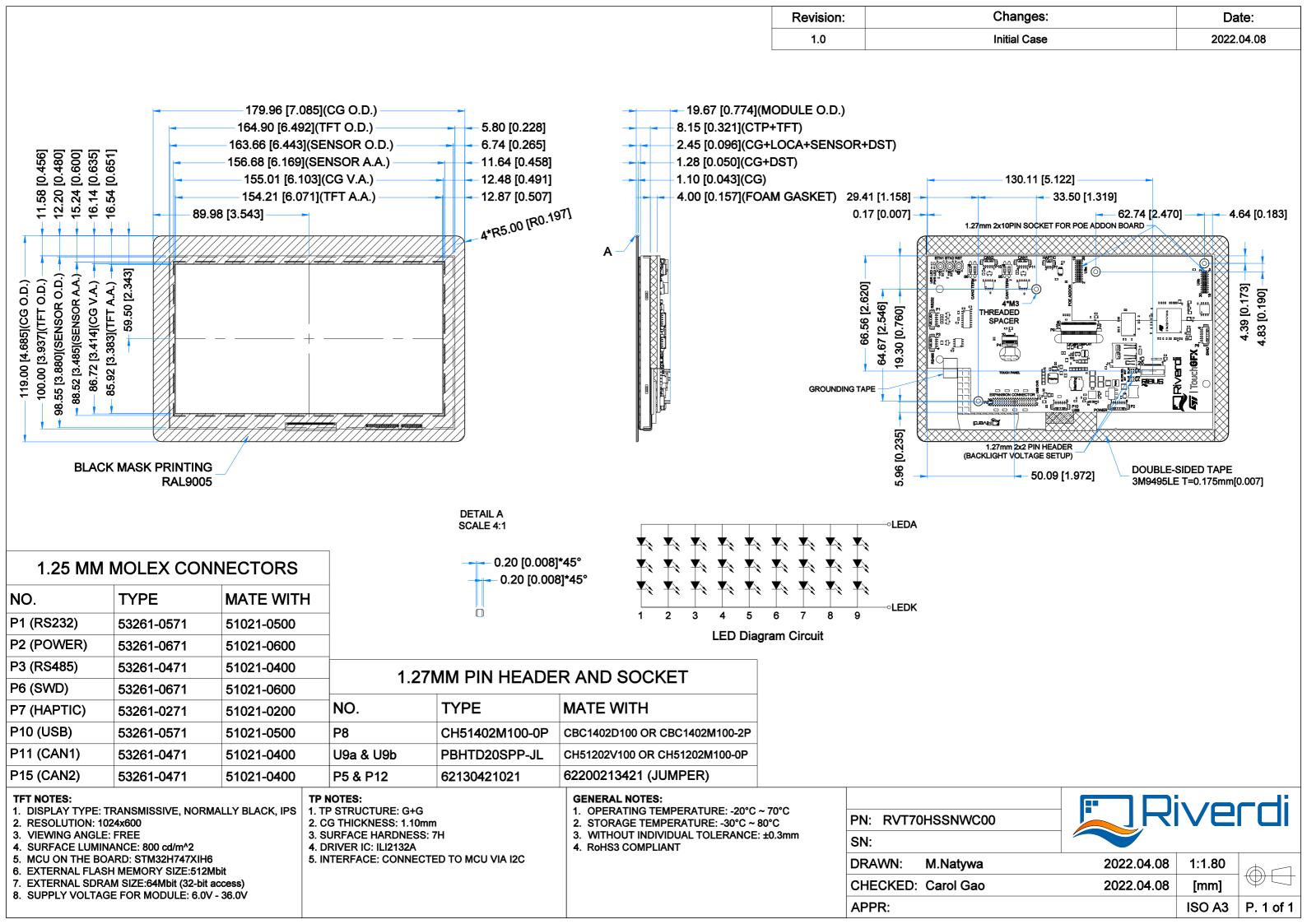


Figure 1. Example of using support brackets





6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	0.0	48.0		Note 1
Digital I/O signals Voltage	-	-0.5	3.3	V	Note 1,2
Operating Temperature	T _{OP}	-20	70	°C	
Storage Temperature	T _{ST}	-30	80	°C	
Storage Humidity (@ 25 ± 5°C)	H _{ST}	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H _{OP}	10	-	% RH	

Note 1. Exceeding maximum values may cause improper operation or permanent damage to the unit.

Note 2. Most of the GPIOs have the 5.0 V tolerant input voltage, please refer to the datasheet of STM32H747XIH6 for more details.

7. ELECTRICAL CHARACTERISTICS

PARAMETER	!	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	for Module	VDD_IN	6.0	12.0	36.0	V
	Current Drawn from VDD_IN @6.0V	I _{VDD_IN=6.0 V}	TBD	TBD	TBD	uA
POWER	Current Drawn from VDD_IN @12.0V	I _{VDD_IN=12.0 V}	TBD	TBD	TBD	uA
'ENABLE' = '0'	Current Drawn from VDD_IN @24.0V	I _{VDD_IN=24.0} V	TBD	TBD	TBD	uA
	Current Drawn from VDD_IN @36.0V	I _{VDD_IN=36.0 V}	TBD	TBD	TBD	uA
	Current Drawn from VDD_IN @6.0V	I _{VDD_IN=6.0 V}	TBD	TBD	TBD	mΑ
POWER	Current Drawn from VDD_IN @12.0V	I _{VDD_IN=12.0 V}	TBD	TBD	TBD	mΑ
'ENABLE' = '1'	Current Drawn from VDD_IN @24.0V	I _{VDD_IN=24.0} V	TBD	TBD	TBD	mΑ
	Current Drawn from VDD_IN @36.0V	I _{VDD_IN=36.0 V}	TBD	TBD	TBD	mΑ
Input Voltage "H" Level		V_{IH}	2.0	-	3.3	V
Input Voltage "I	Input Voltage "L" Level			-	0.8	V

Note. POWER 'ENABLE' refers to pin 4, 'ENABLE' of the power input connector(P2).

By default, POWER 'ENABLE' = '1',

When POWER 'ENABLE' = '0', the device is turned off.

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Lifetime	-	-	50,000	-	hours	Note 1

Note 1. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.



9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf		-	35	35	ms	FIG 2.	4
Contrast Ratio	Cr	θ=O°	800	1000	-		FIG 3.	1
Luminance Uniformity	δ WHITE	ø=0° Ta=25 °C	-	75	-	%	FIG 3.	3
Surface Luminance	Lv	1u-25 C	-	800	-	cd/m²	FIG 3.	2
Viewing Angle		Ø = 90°Ø = 270°Ø = 0°Ø = 180°	75	85	-	deg	FIG 4.	6
	θ		75	85	-	deg	FIG 4.	
Range			75	85	-	deg	FIG 4.	
			75	85	-	deg	FIG 4.	
	Rx		0.578	0.618	0.658	-		
	Ry		0.489	0.329	0.369	-		
	Gx	θ=O°	0.376	0.416	0.456	-		
CIE (x, y)	Gy	ø=0°	0.493	0.533	0.573	-	FIG 3.	5
Chromaticity	Bx	∞-0 Ta=25 °C	0.071	0.111	0.151	-	FIU 3.	5
	Ву	1a-25 C	0.108	0.148	0.188	-		
	Wx		0.270	0.310	0.350	-		
	Wy		0.290	0.330	0.370	-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

Contrast Ratio = $\frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

 $\delta \text{ WHITE } = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is Autronic-Melchers's ConoScope series.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then calculating the average value.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.



Note 7. For viewing angle and response time testing, the testing data is based on Autronic-Melchers's ConoScope series. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, CIE the test data is based on TOPCON's BM-5 photo detector.

Figure 2. The definition of response time

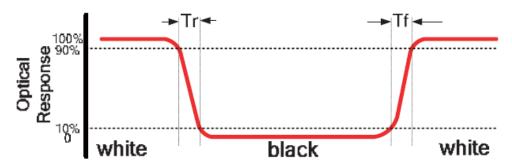
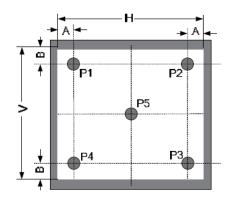


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A: 5mm

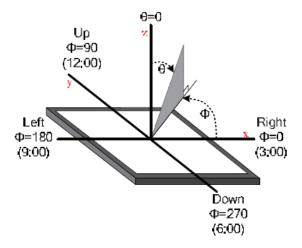
B:5mm

H, V: Active Area

Light spot size Ø=5mm, 500mm distance from the LCD surface to detector lens.

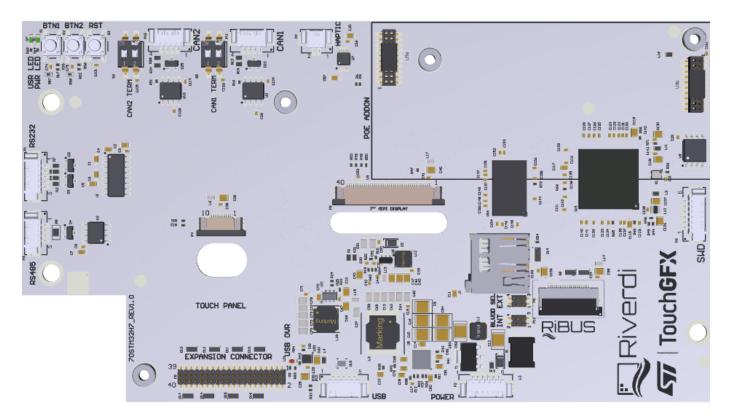
Measurement instrument is TOPCON'S luminance meter BM-5

Figure 4. The definition of viewing angle





10. BOARD INTERFACES AND CONNECTORS



10.1 Power input connector

The 1.25mm, 6-pin Molex connector labeled as "POWER' (P2) is power input connector.

There is an internal reverse polarity protection which ensures that the device is not damaged if the power supply polarity is reversed.

NO.	PIN	DESCRIPTION	NOTE
1	VDD_IN	Power supply input; 6.0-36.0V	
2	VDD_IN	Power supply input; 6.0-36.0V	Note 1
3	VDD_IN	Power supply input; 6.0-36.0V	
4	ENABLE	Enable/ Disable power supply.	Note 2
5	GND	Ground	
6	GND	Ground	

Note 1: STM32 Embedded Display allows to directly connect one additional display to the system. There is RiBUS connector on the board where you can connect any of intelligent display from Riverdi based on EVE4 (BT817Q). However, please note that it may change power supply requirement as below:

The power supply voltage must range between 7.0V-14.0 V (TYP. 9.0 V), if Riverdi EVE4 10.1" series display is connected through RiBUS. At the same time, jumper P5 shall be configured according to subchapter 10.8, note 1.

The power supply voltage ranges between 6.0 V- 36.0V if any of Riverdi EVE4 3.5",4.3",5.0" and 7.0" series display is connected through RiBUS. At the same time, jumper P12 shall be configured according to subchapter 10.8, note 1.



Note 2: By default, pin "ENABLE" is pulled up to VDD and enabled. To disable, ground the pin to turn off the device completely.

10.2 USB interface

The 1.25mm, 5-pin Molex connector labeled as "USB' (P10) is USB interface.

NO.	PIN	DESCRIPTION	NOTE
1	VCC_USB	Power supply	
2	D-	USB data-	
3	D+	USB data+	
4	ID	USB OTG ID; Host /Device detect	Note 1
5	GND	Ground	

Note 1. Configuration of the USB Host/Device mode:

Host Mode: Pin 4 (ID) should be connected to GND.

In this mode, it can provide +5V output voltage to the connected USB device and Max output current 500 mA.

Device Mode: Pin 4 (ID) should be not connected (floating).

10.3 RS485 interface

The 1.25mm, 4-pin Molex connector labeled as "RS485' (P3) is RS485 interface.

NO.	PIN	DESCRIPTION	NOTE
1	VDD_IN	Power supply input; 6.0-36.0V	
2	А	Non-inverting receiver input and non-inverting driver output	
3	В	Inverting receiver input and inverting driver output	
4	GND	Ground	

10.4 RS232 interface

The 1.25mm, 5-pin Molex connector labeled as "RS232' (P1) is RS232 interface.

NO.	PIN	DESCRIPTION	NOTE
1	RTS	Request to send	
2	CTS	Clear to send	
3	TXD	Transmit Data	
4	RXD	Receive Data	
5	GND	Ground	



10.5 2 x CAN FD interfaces

The main board supports 2 channels of the CANFD (Control Area Network Flexible Data-Rate) communication bus, based on the high-speed (2.5-8.0Mbps) CAN transceiver.

2 pcs of 1.25mm, 4-pin Molex connectors labeled as "CAN1' (P11) and "CAN2" (P15) are respectively interfaces of CAN FD1 and CAN FD2.

NO.	PIN	DESCRIPTION	NOTE
1	GND	Ground	
2	CAN_L	CAN Low-Level Voltage	
3	CAN_H	CAN High-Level Voltage	
4	VDD_IN	Power supply input; 6.0 – 36.0 V	

10.6 Haptic feedback connector

The 1.25mm, 2-pin Molex connector labeled as "HAPTIC' (P7) is haptic feedback connector.

Haptic feedback P7 is used to connect with the haptic motor directly.

NO.	PIN	DESCRIPTION	NOTE
1	OUT-	Negative haptic driver differential output	Niata 7
2	OUT+	Positive haptic driver differential output	Note 1

Note 1: The haptic driver DRV2605L is controlled directly by I2C protocol

10.7 SWD (Serial Wire Debug) connector

The 1.25mm, 6-pin Molex connector labeled as 'SWD' (P6) is SWD interface, which is used for programing the MCU on board.

NO.	PIN	DESCRIPTION	NOTE
1	VCC_+3.3V	Power input	
2	SW_CLK	Serial wire clock	
3	GND	Ground	
4	SW_DIO	Serial wire debug data input/output	
5	RST	Reset; Active low	
6	SWO	Serial wire trace output	



10.8 RiBUS

Any size of the Riverdi EVE4 series display can be connected through RiBUS to act as a slave module to the mainboard.

NO.	PIN	DESCRIPTION	
1	VCC_+3.3V	Supply voltage for module; TYP3.3V	
2	GND	Ground	
3	SPI_SCLK	SPI SCK signal	
4	MISO/ IO.1	SPI MISO signal / SPI Quad mode: SPI data line 1	
5	MOSI/ IO.0	SPI MOSI signal / SPI Quad mode: SPI data line 0	
6	CS	SPI chips select signal	
7	INT	Interrupt signal from device to the system, Active Low	
8	RST/PD	Reset / Power down signal, Active Low,	
9	GPIO.0	GPIO.0	
10	DISP_AUDIO	Display audio in/out	
11	GPIO.1/IO.2	SPI Single/Dual mode: General purpose IO0. QSPI mode: SPI data line 2	
12	GPIO.2/IO.3	SPI Single/Dual mode: General purpose IO1. QSPI mode: SPI data line 3	
13	NC	Not connected	
14	NC	Not connected	
15	NC	Not connected	
16	NC	Not connected	
17	BLVDD	Supply voltage for backlight	Note 1
18 BLVDD		Supply voltage for backlight	inote i
19	BLGND	Backlight Ground, internally connected to GND	
20	BLGND	Backlight Ground, internally connected to GND	

Note 1. For Riverdi EVE4 10.1" series displays, the backlight voltage (BLVDD) must range between 7.0 - 14.0V (TYP. 9V).

For Riverdi EVE4 series display ranging from sizes of 3.5" to 7.0", the backlight voltage (BLVDD) shall be 5.0V.

Backlight jumper selectors, P5 and P12, labeled as "BLVDD SEL EXT" and "BLVDD SEL INT are used to configure the backlight voltage range.

Warning! Jumper configuration shall be done when module is not powered.

DO NOT change ANY jumpers while the module has power. Improper operation might cause permanent damage to the unit.

Please pay special attention to not misplace the jumpers. Incorrect jumpers setting may lead to damage to the module as well.



The P5, P12 jumpers setting are shown below.

Table 1. Internal backlight selector P12 setting: 5.0V (default) backlight voltage

SETTING	P5, PIN 1 &2	P5, PIN 3 &4	P12, PIN 1 &2	P12, PIN 3 &4
5.0V (Default)	Open	Open	Short	Short

Table 2. External backlight selector P5 setting: 7.0V-14.0V backlight voltage

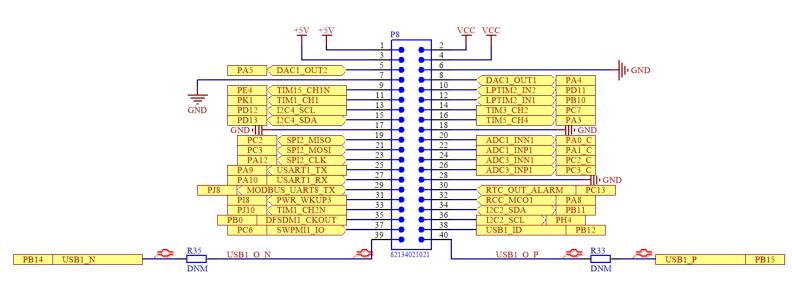
SETTING	P5, PIN 1 &2	P5, PIN 3 &4	P12, PIN 1 &2	P12, PIN 3 &4
7.0V - 14.0 V	Short	Short	Open	Open

10.9 Expansion connector

The main board has one 1.27mm, 40-pin expansion pin header which is labeled as "EXPANSION CONNECTOR" (P8).

It provides direct access to below GPIOs of MCU STM32H747XIH6, which make it possible to easily extend a daughterboard for a specific application.

- 2 x I²C
- 1x UART
- 1x USART
- 1 x SPI
- 1 x USB
- 7 x PWMs
- 2 x DACs (Digital-to-analog)
- 2 x ADCs (Analog-to-digital)



Each of the GPIO pins can be configured by software as output (push-pull or open-drain, with or without pull-up or pull-down), as input (floating, with or without pull-up or pull-down) or as peripheral alternate function. Most of the GPIO pins are shared with digital or analog alternate functions. Please refer to the datasheet of MCU STM32H747XIH6 for more details.



I/O/P	PIN NAME	NO.	NO.	PIN NAME	I/O/P
Р	VCC_+5V	1	2	VCC_+3.3V	Р
Р	VCC_+5V	3	4	VCC_+3.3V	Р
I/O	PA5	5	6	GND	Р
Р	GND	7	8	PA4	1/0
I/O	PE4	9	10	PD11	1/0
I/O	PK1	11	12	PB10	1/0
I/O	PD12	13	14	PC7	1/0
I/O	PD13	15	16	PA3	1/0
Р	GND	17	18	GND	Р
I/O	PC2	19	20	PA0_C	1/0
I/O	PC3	21	22	PA1_C	1/0
I/O	PA12	23	24	PC2_C	1/0
I/O	PA9	25	26	PC3_C	1/0
I/O	PA10	27	28	GND	Р
I/O	PJ8	29	30	PC13	1/0
I/O	P18	31	32	PA8	1/0
1/0	PJ10	33	34	PB11	I/O
1/0	PB0	35	36	PH4	I/O
1/0	PC6	37	38	PB12	1/0
1/0	PB14	39	40	PB15	I/O

Note 1. The total output current I@5.0V from pin 1 and pin3 is maximum 1A.

The total output current I@3.3V from pin 2 and pin 4 is maximum 500mA.

Note 2. The 'USR LED' is connected to pin 33, PJ10, of the expansion connector.

By default, the resistor R60, (0402, 1k ohms) is soldered. Please remove R60 to use GPIO channel PJ10.

Note 3. Push button BTN2(S2) is connected to pin 35, PBO, of the expansion connector.

Please remove R58 to use GPIO channel PBO.

Push button BTN1(S1) is connected to pin 37, PC6, of the expansion connector.

Please remove R57 to use GPIO channel PC6.

10.10 Micro SD slot

The mainboard is equipped with Micro-SD slot, which supports all types of Micro SD cards.

10.11 2 x 20-pin, 1.27 mm pin sockets for POE Add-on Board

The 2 x 20-pin, 1.27 mm, pin sockets, labeled as U9a and U9b, are used to connect the Riverdi POE Add-on Board.

The Riverdi POE Add-on Board features 10/100M Ethernet Port with Power-Over-Ethernet enabled. It allows you to power the module through the Ethernet port.

Note. The Riverdi POE Add-on Board is offered as an accessory.



11. USER INTERFACES

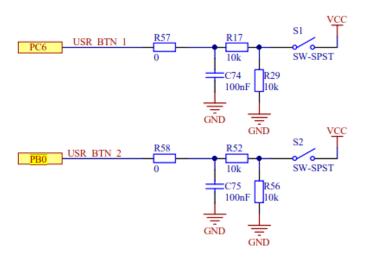
11.1 3 x push buttons

Push button labeled as "RST" is used to "RESET" the module.

Another 2 push buttons labeled as BTN1, BTN2 are for user's development.

- BTN1(S1) is connected to pin 37, PC6, of the expansion connector.
- BTN2(S2) is connected to pin 35, PBO, of the expansion connector.

By default, pushbuttons BTN1(S1), BTN2(S2) are enabled. To use GPIO channel PC6 and PB0, R57 and R58 should be removed.



11.2 3 x LEDs

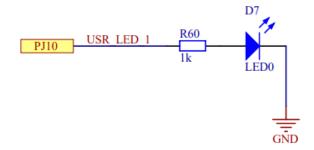
1 x LED, labeled as 'PWR LED', emits green light when the modules is powered.

1 x LED, labeled as 'USB OVR', emits red light when VCC_USB pin is shorted.

1 x LED, labeled as 'USR LED', is for user's development.

• The 'USR LED' is connected to pin 33, PJ10, of the expansion connector.

By default, the resistor R60, (0402, 1k ohms) is soldered. Please remove R60 to use GPIO channel PJ10.





12. DISPLAY SEPCIFICATION

12.1 TFT resolution

The supported resolution of the display in this module is 1024*600.

12.2 Full TFT specification

For detailed information on the display, please refer to datasheet of display RVT70HSMNWC00.

13. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

13.1 Mechanical characteristics

DESCRIPTION SPECIFICATION		REMARK
Touch Panel Size	7.0 inch	
Outline Dimension of CTP	179.96 mm x 119.00 mm	
Product Thickness	2.45 mm	
Glass Thickness	1.1 mm	uxTouch
CTP View Area	155.01 mm x 86.72 mm	
Sensor Active Area	156.68 mm x 88.52 mm	
Surface Hardness	7H	

13.2 Electrical characteristics

DESCRIPTION		SPECIFICATION	REMARK
Power Consumption (IDD)	Active Mode	90 mA	
Power Consumption (IDD)	Sleep Mode	10 mA	
Linearity		+/- 1.5mm	
Controller		ILI2132A	
Resolution		1024 x 600	



14. INSPECTION

Standard acceptance/rejection criteria for TFT module

14.1 Inspection condition

Ambient conditions:

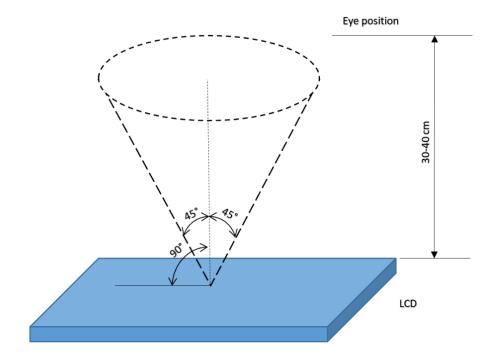
• Temperature: 25 ± 2°C

• Humidity: (60 ± 10) %RH

• Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

Viewing Angle: U/D: 45°/45°, L/R: 45°/45°





14.2 Inspection standard

ITEM		CRITE	RION				
				Size = 7"			
Black spots, white spots,	x	Average Dia	Average Diameter D < 0.2 mm		Qualified Qty Ignored		
light leakage,	•	0.2 mm < D	≤ 0.3 m	nm	N≤3	3	
Foreign Particle (round Type)		0.5mm < D			Not	allowed	
	D=(x+y)/2 Spot's density: 10 mm	0.5mm < D					
	Width			Size = 7"			
	<u> </u>	Length		Width		Qualified Qty	
LCD black spots, white spots,)	-		W ≤ 0.0	5	Ignored	
light leakage (line Type)	Length	L ≤ 5.0		0.05 < W ≤	60.1	3	
31 /		5.0 < L		0.1 < W	,	Not allowed	
		Size = 7"					
Bright/Dark	ltem		Qualified Qty				
Dots	Bright dots		N≤2				
Dots	Dark dots			1	N≤3		
	Total bright and dark dots		N≤4				
	Size						
	Average Diameter		Qualified Qty				
Clear spots	D < 0.2 mm		Ignored				
•	0.2 mm < D < 0.3		4				
	0.3 mm < D < 0.5 mm		2				
	0.5 mm < D		0				
	• 5.	Size = 7"					
Polarizer	Average Diame	eter				ied Qty	
bubbles	D ≤ 0.2 mm		Ignored				
	0.2 mm < D ≤ 0.5	mm	4				
	0.5 mm < D	Cizo	0 Size ≥ 5"				
	Average Diame			Quali	ifiod	Otv.	
Touch panel	D < 0.25 mm		Qualified Qty				
spot			Ignored 4				
	0.25 mm < D < 0.5 mm 0.5 mm < D		0				
	_	Size	≥ 5"				
Touch panel	Length	Width		Qua	alified	d Qty	
White line	-	W < 0.03			ored	-	
Scratch	L < 5.0	0.03 < W < 0	_				
	-	0.05 < W	0				



15.RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	Note 1
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40°C, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20°C for 30mins, 70°C for 30 mins. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Note 1. Sample quantity for each test item is $5 \div 10$ pcs.

Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



16.LEGAL INFORMATION

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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